# SIDRA: Safe IC-Design for Robust Application

Robust operation of complex power and mixed analogue/digital devices in harsh environments is essential for the electronic systems deployed increasingly to monitor vital control and safety functions in modern motor vehicles. Similar reliability standards are necessary in many other applications, ranging from security to communications and even some consumer appliances. A particularly important requirement is to ensure that such components are resistant to damage caused by electrostatic discharge (ESD). The project SIDRA is responding to this need by developing simulation-guided design methods that for the first time will aim to address ESD protection at a whole-chip level. ESD occurs during production as well as later in use and is a potential "chip killer". The project is processed on national and European level (MEDEA+ T104). The effects of the project work will be ESD protection of ICs in future technologies, i.e. in safety critical applications like automotive, identification systems and chip cards, the risk minimization of expensive and image damaging problems in the field and the achievability of specification demands, which are not possible today due to a matter of robustness.

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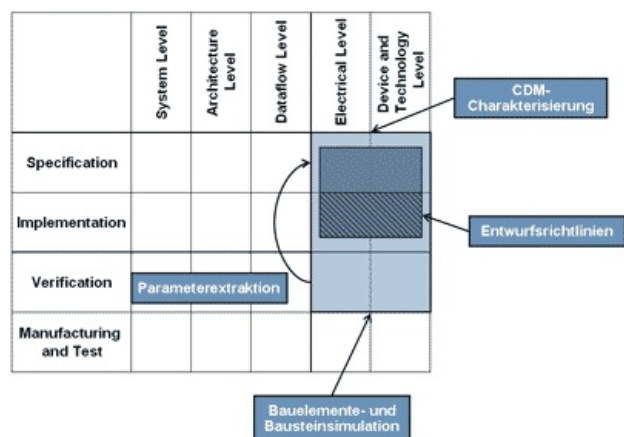
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- Atmel Germany GmbH
- Infineon Technologies AG
- Robert Bosch GmbH
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Used Abbreviations

Abbreviation	Meaning
PR	Project Report
SPR	Short Project Report
PN	Project News
FPR	Final Project Report

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