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edaForum03 - Program

At this web page you find the programme of the edaForum03. You may expand the programme for each session by clicking on the session title. You will find the detailed timetable, presentation titles and author names. If additional information like an abstract, curriculum vitae or (for attendees of the edaForum03 only) slides is available, a link below the presentation title is displayed.

You may [download](#) the programme as PDF file (3.16 MB).

Thursday, November 6, 2003

08:30 - 16:00

Registration Opening Time

09:00 - 12:00

Company Presentations

Moderator: Erich Barke (edacentrum)

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- | | |
|-------|--|
| 09:00 | Mentor Graphics Corp.: Verification Languages and where they fit
Abstract and Curriculum Vitae |
| 09:00 | Cadence Design Systems, Inc: "Unified Verification Methodology" and "Custom IC Platform"
Abstract and Curriculum Vitae |
| 09:00 | Doulos Ltd.: Challenges and Solutions for ESL Design and Verification
Abstract and Curriculum Vitae |
| 09:00 | Verisity Design Inc: Verification: Can't Live With It, Can't Live Without It
Abstract and Curriculum Vitae |
| 09:00 | Synchronicity, Inc.: Building Reuse in an SoC Development Environment
Abstract and Curriculum Vitae |

10:30 - 10:50

Coffeebreak

12:00 - 13:00

Lunch

13:00 - 13:15

Welcome Address

Moderator: Erich Barke (edacentrum)

13:15 - 14:15

Keynote

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- | | |
|-------|--|
| 13:15 | Designing Robust Systems with Uncertain Information
Giovanni De Micheli (U Stanford)
Abstract and Curriculum Vitae
Proceedings (Access for event attendees only) |
|-------|--|

14:30 - 16:45

Technical Session 1 : "Devil in Disguise - The Deep Submicron Hell of Physical Design"

Moderator: Erich Barke (edacentrum)

Keynote:**The Design-Manufacturing Roadmap**

14:30 Andrew B. Kahng (U California, San Diego, US)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

15:15 **Break:**

Coffee Break**Semi-Hierarchical Layout Approaches for ASIC Designs with Multi-Million Instances**

15:45 Markus Bühler (IBM)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

Design Closure Starts With DESIGN

16:15 Jacques Benkoski (Monterey)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

14:30 - 16:45**Business Session 1: "Industrial Disease - Measure or Die - Design Productivity"**

Moderator: Peter van Staa (Robert Bosch GmbH)

Keynote:**Economics of IP**

14:30 Joachim Kunkel (Synopsys)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

15:15 **Break:**

Coffee Break**Design Cost Modeling and Data Collection Infrastructure**

15:45 Andrew B. Kahng (U California, San Diego, US)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

IBM ASIC Design TAT Reduction

16:15 Jürgen Koehl (IBM)

[Abstract and Curriculum Vitae](#)

16:45 - 17:15

Break

17:15 - 18:00**Panel Discussion: Discuss with the today's presenters their views about EDA****Room Berlin I**

18:00 - 19:30

Break

19:30 - 23:30**Social Event**

The SI-Entertainment-Center is known for its "Colonnaden": Nineteen themed restaurants and bars which offer exciting wining & dining in different international styles. We invite you to enjoy the culinary nightlife of the SI-Center and offer you an exclusive evening in the Brasserie Le Jardin – a typical French styled restaurant.

Appropriate to the European EDA-Week a culinary trip through Europe is expecting you with exquisite French cuisine, delights mediterranean and nordish specialities. Beside the excellent dinner buffet, a special guest expects you to take you away to the world of magic and mystery.... Enjoy a delightful and communicative meeting - an exclusive event and great entertainment.

Friday, November 7, 2003

08:30 - 09:30

Registration Opening Time

09:00 - 11:45

Business Session 2: "Money - Seed, Care and Harvest - Value Focused EDA"

Moderator: Jürgen Haase (edacentrum, D)

Keynote:

09:00 The Revenge of Economics over Engineering

Jacques Benkoski (EDA Consortium)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

GLM at Infineon - Effective License Management for Efficient Asset Utilization

09:45 Klaus Köppel (Infineon)

[Abstract and Curriculum Vitae](#)

**10:15 Break:
Coffee Break**

VC Funding for EDA Start-Ups: Pain or Gain? A Founder's View

10:45 Wolfgang Nebel (University of Oldenburg, D)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

VC Funding for EDA Start-Ups: Pain or Gain? A VC's perspective

11:15 Andreas Demleitner (BayTech)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

09:00 - 11:45

Technical Session 2: "Stairway to Heaven - The 7th Heaven of System Level Design"

Moderator: Wolfgang Rosenstiel (edacentrum, D)

Keynote:

An Overview of System-Level Design: Current Status, Future Possibilities

09:00 Grant Martin (Cadence)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

Virtual Prototyping

09:45 Carsten Mielenz (Infineon)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

**10:15 Break:
Coffee Break**

Transaction Level Modelling in SystemC

10:45 Mark Burton (ARM)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

SystemVerilog at the Transaction Level

11:15 Peter Flake (Synopsys)

[Abstract and Curriculum Vitae](#)

[Proceedings \(Access for event attendees only\)](#)

11:45 - 12:00

Break

12:00 - 13:00

Panel Discussion: Discuss with the today's presenters their views about EDA

Room Berlin I

Social Event:

12:00 Conferment of edacentrum's "EDA Achievement Award"

[Further Information](#)

12:30 - 14:00

Registration Opening Time

13:00 - 14:00

Lunch

14:00 - 23:30

Trips and Tours

Mercedes Benz Plant Sindelfingen

Abstract: Württemberg inventors were and still are the driving force behind the successful economic development of the region. The triumphant march of the automobile began here in Stuttgart.

14:00 Since 1953 the Sindelfingen plant has been offering you the opportunity to collect your new Mercedes-Benz directly from the factory. Experience the highlights of production of a Mercedes-Benz passenger car. The

Sindelfingen plant produces vehicles from the C, E, S and CL Class ranges. A guided tour makes it possible to take a look behind the scenes. (expected to end ca. 5:30 pm)

The participation capacity of the plant tour is limited we will give the place to those who register first.

TV Tower

14:00 **Abstract:** The first TV tower to be built of reinforced concrete and prototype for nearly all TV towers throughout the world. Built in 1954 - 1956 by the architects Leonhardt, Gutbrod and Heinle it stands 217 metres high with its 4-level "basket" with restaurant and sightseeing platform at 150 metres above the ground and 400 metres above Stuttgart downtown as well as a further restaurant and terrace at the foot of the tower. A modern Stuttgart landmark.

A guided tour through the building gives information about the TV tower, its architecture and its broadcast equipment from the foundation up to its aerials. (expected to end ca. 4:00 pm)

State Gallery Stuttgart

14:00 **Abstract:** The Old State Gallery (1838 - 1843, Architect G.G. Barth) presents art from the Middle Ages up to the 19th century, old German, Italian, Dutch paintings and sculpture from Classic up to Impressionist. The New State Gallery (1979 - 1984 Architect J. Sterling) is recognised today as one of the main works of "Postmodern architecture" and shows art from the 20th century: main collections Modern Classic: Matisse, Picasso, Beckmann, Schlemmer and art from 1945 onwards: Beuys, Newman, Pollock, Kiefer, Paik and more. Furthermore it has an appealing inner courtyard with sculptures, rooms displaying permanent collections and alternating exhibition rooms. (expected to end ca. 4:00 pm)

[Further information](#)

8. European SystemC User Group Meeting (ESCUG)

14:00 **Abstract:** The European SystemC Users Group will traditionally provide possibilities for SystemC users to present and discuss their SystemC design experience. This is one of the major intentions of the European SystemC Users Group Meetings.

[Further information](#)

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