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Welcome to HoLoDEC

In the research project "Design methods and HW/SW co-verification for the unique identifiability of electronic components" - HoLoDEC for short - experts from research institutions and industry are exploring new development methods for trustworthy intellectual property (IP) and its integration along global value chains. Twelve partners from research and science as well as from the electronics and user industries are working together under the coordination of Infineon to develop a holistic security concept.

HoLoDEC has set itself the goal of transforming hardware from an Achilles' heel to a foundation of trustworthiness, making use of the immutability of hardware after production, similar to a fingerprint. To this end, a holistic security concept is being developed that uses innovative methods to secure individual IP components and their integration into an overall system against security risks, attacks and manipulation by third parties. The basis for this is a novel IP design and verification flow that will ensure trustworthiness, especially in security-critical electronic systems.

HoLoDEC mit drei Postern auf ADTC & edaWorkshop24 in Dresden

2024/05/05



Auf ADTC und edaWorkshop24 am 9. und 10. April 2024 in Dresden war HoLoDEC mit drei Postern in der Posterausstellung präsent. Vor den Postern gab es einen regen Austausch zu den Ergebnissen, dessen Impulse in die weiteren Arbeiten einfließen.

HoLoDEC präsentiert sich zum edaWorkshop23 in Hannover

2023/06/10



Zum edaWorkshop23, der am 8+ und 9. Juni 2023 in Hannover standfand, war HoLoDEC mit einem Übersichtsposter und einem Poster mit ersten Ergebnissen in der Posterausstellung vertreten.

HoLoDEC stellt sich zum OCTOPUS-Auftakt vor

2023/01/25



Bei der digitalen OCTOPUS-Auftakveranstaltung am 24. Januar 2023 hat sich HoLoDEC mit einem Übersichtsvortrag vorgestellt.

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