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edaWorkshop09 - Call for Papers

Submission of Contributions

Download the Call for Papers here [1]

In addition to the presentation of EDA research projects and their results, the edaWorkshop aims at the publication of industrially-relevant R&D results covering topics listed below.

Expected are submissions to the following categories:

1. Scientific contributions from research and industry which present new EDA research and development results

2. Presentations of EDA topics of a visionary or survey character, with scientific or practical impact

3. Contributions to the application relevance of and the economical impact on technical challenges or solutions

4. Reports on experiences or on the dissemination of results from industrial practice

5. Demonstrations of research and development results, in particular those from Ekompass projects

The program committee consisting of leading EDA experts (named overleaf) from industry and research, will review the contributions by category in order to devise a program of presentations, posters and demonstrations. Accepted contributions will be published in the edaWorkshop proceedings, which will appear in the VDE-Verlag with an ISBN. The proceedings will not distinguish between poster, presentation and demonstration contributions – all contributions are equally important to our common goal.

Key Dates

November 11, 2008
Submission of papers

December 19, 2008
Notification of acceptance

February 16, 2009
Submission of camera-ready papers

May 26 – 28, 2009
edaWorkshop in Dresden

Modalities

Scientists and users are invited to submit contributions in due time without author and company names in five to six pages, in German or English, at events/edaworkshop/2009/upload [2].

Guidelines for authors as well as the templates (Word and LaTeX) can be found here:

Templates and guidelines for LaTeX [3] (at the moment in German only)
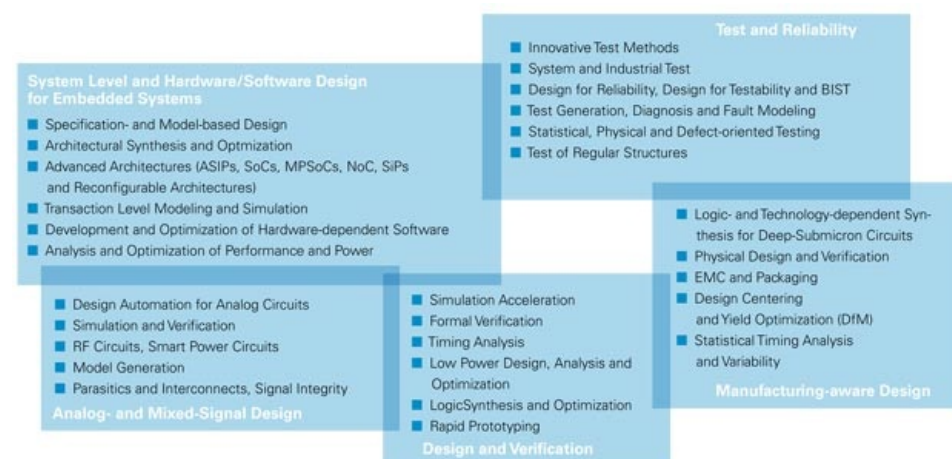Templates and guidelines for Word [4] (at the moment in German only)

If you urgently need an English version, please get in contact with us.

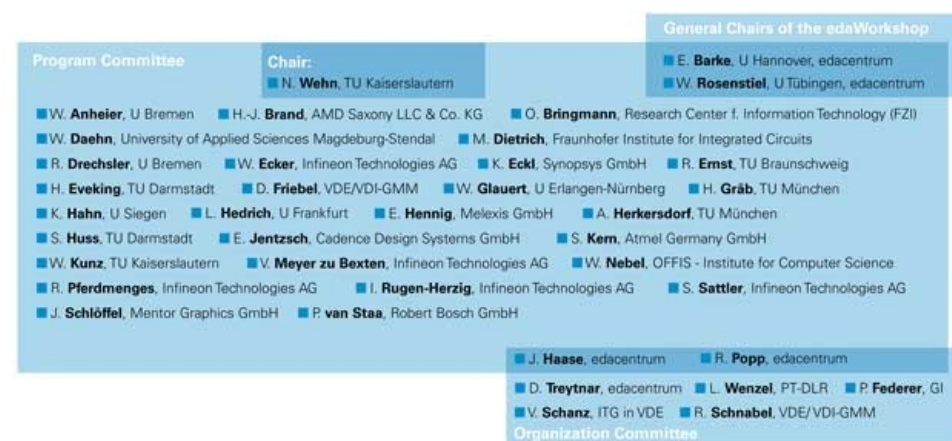The Call for Papers as a pdf for download can be found here [1]

Conference language will be English. The duration of oral presentations will be about 20 minutes.

edaWorkshop09 Topics

Submissions covering the following topics are welcome:

System Level and Hardware/Software Design for Embedded Systems
- Specification- and Model-based Design
- Architectural Synthesis and Optmization
- Advanced Architectures (ASIPs, SoCs, MPSoCs, NoC, SiPs and Reconfigurable Architectures)
- Transaction Level Modeling and Simulation
- Development and Optimization of Hardware-dependent Software
- Analysis and Optimization of Performance and Power

Test and Reliability
- Innovative Test Methods
- System and Industrial Test
- Design for Reliability, Design for Testability and BIST
- Test Generation, Diagnosis and Fault Modeling
- Statistical, Physical and Defect-oriented Testing
- Test of Regular Structures

Analog- and Mixed-Signal Design
- Design Automation for Analog Circuits
- Simulation and Verification
- RF Circuits, Smart Power Circuits
- Model Generation
- Parasitics and Interconnects, Signal Integrity

Design and Verification
- Simulation Acceleration
- Formal Verification
- Timing Analysis
- Low Power Design, Analysis and Optimization
- LogicSynthesis and Optimization
- Rapid Prototyping

Manufacturing-aware Design
- Logic- and Technology-dependent Synthesis for Deep-Submicron Circuits
- Physical Design and Verification
- EMC and Packaging
- Design Centering and Yield Optimization (DfM)
- Statistical Timing Analysis and Variability

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