edaWorkshop15 - Call for Papers

Submission of Contributions

Download the Call for Papers here [1].

The submission deadline is on January 19, 2015. To submit a contribution click here [2].

In addition to the presentation of EDA research projects and their results, the edaWorkshop aims at the publication of industrially-relevant R&D results covering topics listed below.

Expected are submissions to the following categories:

1. Scientific contributions from research and industry which present new EDA research and development results
2. Presentations of EDA topics of a visionary or survey character, with scientific or practical impact
3. Contributions to the application relevance of and the economical impact on technical challenges or solutions
4. Reports on experiences or on the dissemination of results from industrial practice
5. Demonstrations of research and development results, in particular those from ICT2020 or CATRENE projects
6. Presentations or sessions on R&D projects demonstrating the applications of microelectronics.

Starting this year, also submissions are encouraged which address the increasing need to consider application aspects during the design process, in particular bridging the gap in the value chain between applications and chip design.

The program committee consisting of leading EDA experts (named below) from industry and research, will review the contributions by category in order to devise a program of presentations, posters and demonstrations. Accepted contributions will be published in the edaWorkshop proceedings, which will appear in the VDE-Verlag with an ISBN. The proceedings will not distinguish between poster, presentation and demonstration contributions – all contributions are equally important to our common goal.

Conference language will be English.

Key Dates

Submission deadline: January 19, 2015
Submission of papers, go here [2] for submission

February 16, 2015
Notification of acceptance

March 30, 2015
Submission of camera-ready papers

May 19 – 21, 2015
edaWorkshop in Dresden

Modalities

Scientists and users are invited to submit contributions in due time without author and company names on five to six pages, In German or English, preferably in English, at edaworkshop/upload [2].

Guidelines for authors as well as the templates (Word and LaTeX) can be found here:

- Templates and guidelines for LaTeX [3] (includes German and English)
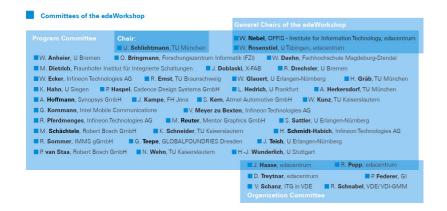- Templates and guidelines for Word [4] (includes German and English)

If you urgently need an English version, please get in contact with us. The Call for Papers as a pdf for download can be found here [1]. Conference language will be English. The duration of oral presentations will be about 20 minutes.

edaWorkshop15 Topics

Submissions covering the following topics are welcome:

edaWorkshop15 Topics

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System Level and Hardware/Software Design of Embedded Systems
- Specification- and Model-based Design
- Architectural Synthesis and Optimization
- Advanced Architectures (ASIPs, SoCs, MPSoCs, NoC, SiPs and Reconfigurable Architectures)
- Transaction Level Modeling and Simulation
- Development and Optimization of Hardware-dependent Software

Design and Verification
- Formal Verification
- Statistical Timing Analysis and Variability
- Low Power Design, Analysis and Optimization
- Logic- and Technology-dependent Synthesis for Nanometer Circuits
- Physical Design and Verification
- Simulation Acceleration and Rapid Prototyping
- Productivity and Efficiency of Design

- Design Automation for Analog Circuits
- Synthesis, Simulation and Verification
- RF Circuits, Smart Power Circuits
- Model Generation
- Parasitics and Interconnects
- Signal Integrity and EMC

Analog- and Mixed-Signal Design

- 3D Design, Packaging and SiP
- Design for Integration of Multi-Domain components
- Energy Efficient Design
- Analysis and Optimization of Performance and Power
- Cyber-Physical Systems
- Design for New Technologies
- Design for Specific Applications

More than Moore

- Design for Reliability and Robustness
- Modeling of Aging Effects
- Design Centering and Yield Optimization (DfM)
- Fault-tolerant and Self-healing System Design
- System Test and Production Test
- Delay Test and Defect-oriented Test
- BIST and Design for Testability
- Test Generation, Diagnosis and Fault Modeling
- Test of Regular Structures

Reliability, Robustness and Test

Committees of the edaWorkshop

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Organization Committee

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Source URL: https://project.edacentrum.de/en/events/edaworkshop/2015/call

Links:
[1] https://project.edacentrum.de/system/files/files/edaworkshop/2015/edaWS15-call.pdf
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