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edaForum05 - Program

At this web page you find the programme of the edaForum05. You may expand the programme for each session by clicking on the session title. You will find the detailed timetable, presentation titles and author names. If additional information like an abstract, curriculum vitae or (for attendees of the edaForum05 only) slides is available, a link below the presentation title is displayed.

You may [download](#) the programme as PDF file (594.98 K).

Thursday, November 17, 2005

08:30 - 09:00

Comming Together

09:00 - 12:00

Company Presentations

09:00 **Yield and Manufacturability:the 4th Dimension in Design Optimization**

[Abstract and Curriculum Vitae](#)

09:00 **EDA Tools and Methodologies: A Synopsys Perspective**

[Abstract and Curriculum Vitae](#)

09:00 **Reducing Verification Risk to Increase overall prifitability**

[Abstract and Curriculum Vitae](#)

09:00 **Hot spots in IC Nanometer Design**

[Abstract and Curriculum Vitae](#)

10:20 **Break:
Coffee Break**

12:00 - 13:00

Lunch

13:00 - 13:15

Welcome Address

Moderator: Wolfgang Rosenstiel (edacentrum, D)

Salon Langenhagen

13:15 - 14:15

General Keynote

Salon Langenhagen

Quo Vadis, EDA? Reasoning about the Trends andChallenges of Engineering Design Automation

13:15 Alberto L. Sangiovanni-Vincentelli (U Berkeley)

[Abstract and Curriculum Vitae](#)

14:15 - 14:30

Break

14:30 - 17:15

Technical Session I: "I'VE GOT THE POWER"The Power of Low Power Design

Keynote:

Green Chips - Design for Low Energy

14:30 Wolfgang Nebel (ChipVision)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

15:15 **Break:**
Coffee Break

Ruchir Puri, IBM: "Keeping Hot Chips Cool"

15:45 Ruchir Puri (IBM)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

Multi-Mode Power Reduction for Mobile Application Chips

16:15 Knut Just (Infineon)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

14:30 - 17:15

Business Session I: "LET'S WORK TOGETHER" Chip Design Is A Management Problem

Keynote:

Automotive Electronics: A walk on the Ridge between Innovation and Quality

14:30 Claus Schmidt (Bosch)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

15:15 **Break:**
Coffee Break

The CELL Microprocessor

15:45 Silvia Melitta Müller (IBM)
[Abstract and Curriculum Vitae](#)

A useful way to view SoC platforms

16:15 Mitch Mastellone (MatrixOne)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

Chip Design: It's Not Just for Techno-Nerds Anymore

16:45 Jay Greenberg (Synopsys)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

17:15 - 17:30
Break

17:30 - 18:15

Panel Discussion

Moderator: Wolfgang Rosenstiel (edacentrum, D)

18:15 - 19:00
Break

19:00 - 23:00

Social Event

Dinner:

19:00 **Comedy and Melody Dinner Conferment of edacentrum's "EDA Achievement Award"**
[Abstract and Curriculum Vitae](#)

Friday, November 18, 2005

08:30 - 09:15

Friday Keynote

Design Challenges for Mobile Devices

08:30 Hermann Eul (Infineon)
[Abstract and Curriculum Vitae](#)

09:15 - 09:30
Break

09:30 - 12:15

Technical Session II: "SOLID AS A ROCK" Designing Robust Systems unter Uncertain Conditions

Keynote:

Designing Reliable Systems with Unreliable Components

09:30 Shekhar Y. Borkar (Intel)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

Statistical Analysis and Design of Digital Integrated Circuits

10:15 Chandu Visweswariah (IBM)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

Break:

Coffee Break

Statistical Design - an Incomplete Solution

11:15 Louis Scheffer (Cadence)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

Analog Circuits: What is your Worst Case?

11:45 Ulf Schlichtmann (TU München, D)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

09:30 - 12:15

Business Session II: "MÄNNER" Wise Men Buy EDA - Real Men Build Fabs

Keynote:

Competitive Re-Aggregation: Opportunities for EDA

09:30 Daya Nadamuni (Gary Smith EDA)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

Collaborative Engineering and Outstanding Expertise as Key Differentiators for 65 nm Success

10:15 Heinz Schützeneder (Intel)
Bio: IFX Datensatz benötigt
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

Break:

Coffee Break

If Wise Men Buy EDA, and Real Men Build Fabs - then Successful Men Are Absolutely Fabless

11:15 Cesar Martin Perez (MIPS)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

Design Enablement for 90 nm and Beyond - The New Paradigm in the Semiconductor Industry

11:45 Anton Ossner (Chartered)
[Abstract and Curriculum Vitae](#)
[Proceedings \(Access for event attendees only\)](#)

12:15 - 12:30

Break

12:30 - 13:15

Panel Discussion

Moderator: Erich Barke (edacentrum)

13:15 - 14:00

Lunch

14:00 - 16:00

Trips and Tours

14:00 **Field Trip to Sennheiser electronic GmbH**

edacentrum | Schneiderberg 32 | 30167 Hannover | fon: +49 511 762-19699 | email: [info@edacentrum](mailto:info@edacentrum.de) [dot] deup

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