

## Scale4Edge Partner MINRES @ Accellera's SystemC Evolution Fika

2021/03/16

**March 17, 2021**  
**16:00 - 18:00 CET**  
**Virtual event**

Accellera's SystemC Evolution events are expanding with the addition of SystemC Evolution Fikas! Fika is a tradition of sharing a coffee, slowing down a bit, and talking about things that we care about.

The first SystemC Evolution Fika will take place on March 17 from 16:00 to 18:00 CET. It will be free of charge and virtual. There are two presentations planned: one on SystemC and Python and one about the Intel SystemC Compiler.

We hope you join us! If you have topics that you think should be included in upcoming fikas, please let us know at [systemc-evolution-fika@lists \[dot\] accellera \[dot\] org](mailto:systemc-evolution-fika@lists[dot]accellera[dot]org) (**[systemc-evolution-fika@lists \[dot\] accellera \[dot\] org](mailto:systemc-evolution-fika@lists [dot] accellera [dot] org)**).

This event is free but **registration** <sup>[1]</sup> is required.

### Python and SystemC

**16:00 - 17:00**

*Rocco Jonack, Arteris; Eyck Jentzsch, MINRES Technologies*

Scripting is commonly used in today's applications and EDA tools. For SystemC various proprietary and open source solutions are available. All of them impose various constraints on SystemC users and often restrict the visibility of C++ components to Python. We present a novel approach of integrating SystemC. It does not require any instrumentation or manual preparation and exposes all SystemC types and functions as well as components provided by other libraries, i.e. IP libraries (even in binary form). It allows calling Python from SystemC modules such as to implement scriptable components for verification purposes. The session will outline how this can be used to implement interactive and dynamic tools to assemble Virtual prototypes (VP) easily as well as control their simulation. This is especially useful in HW/SW unit testing and FW verification where the dynamic nature of Python allows to select various test cuts of the VP to ease the tasks.

---

The Scale4Edge project (project label 16ME0122K-140, 16ME0465, 16ME0900, 16ME0901) is supported by the German Federal Ministry of Research, Technology and Space (**BMFTR**).

---

**Source URL:** <https://project.edacentrum.de/scale4edge/en/scale4edge-systemc-evolution-fika>

### Links:

[1] <https://cts.vresp.com/c/?AccelleraSystemsInit/1794fba52a/d113cd2460/23fcdbc475>