

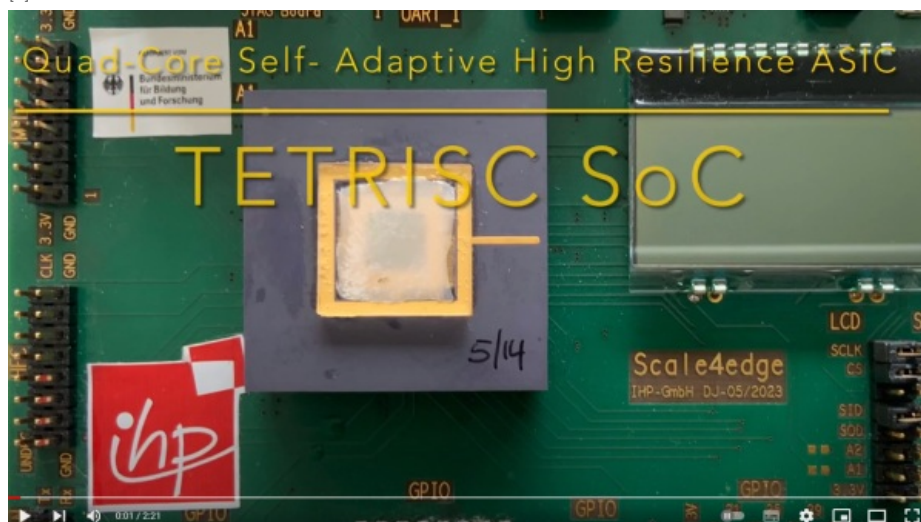
## TETRISC SoC — Quad RISC-V Core Self-Adaptive High-Reliability ASIC from IHP GmbH

2024/05/30

The TETRISC SoC is a demonstrator of results that have been researched and developed in the Scale4Edge project. The video is played on YouTube if you click on the image below.



[1]



The Scale4Edge project (project label 16ME0122K-140, 16ME0465, 16ME0900, 16ME0901) is supported by the German Federal Ministry of Research, Technology and Space (BMFTR).

**Source URL:** <https://project.edacentrum.de/scale4edge/en/tetrisc-soc-%E2%80%94-quad-risc-v-core-self-adaptive-high-reliability-asic-ihp-gmbh>

**Links:**

[1] [https://youtu.be/mmyK5EotTIY?si=AQs5kUmffTtA\\_9vRk](https://youtu.be/mmyK5EotTIY?si=AQs5kUmffTtA_9vRk)